

# High Frequency Integrated Circuits The Cambridge Rf And Microwave Engineering Series

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LNA Design Methodology - University of Delaware

22.10.2018 · LNA Design | Goal is to maximize LNA figure of merit: FoM LNA =  $G \cdot \text{IIP}_3 \cdot f_0 \cdot (F \cdot 1)^{-1} \cdot P$  | To minimize  $F = F_{\min} + R_n$   
 $G \cdot S_{YY} \cdot S_{YY}^*$  | we require noise match, i.e.  $R_{\text{SOPT}} = 1$   $G \cdot S_{ZZ} = Z_0 = 50\Omega$  | To maximize power gain (G), we require input impedance match, i.e.  $R_{\text{in}} = Z_0 = 50\Omega$ , and also conjugate match at the output. | The imaginary components of  $Z_{\text{SOPT}}$  and  $Z_{\text{in}}$